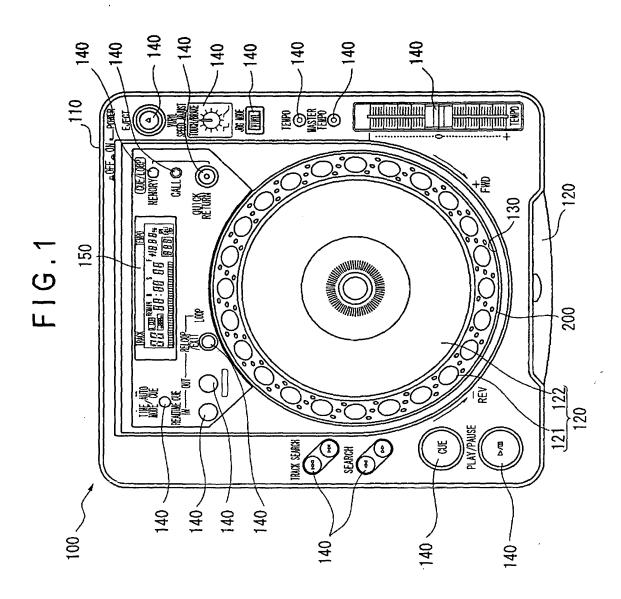
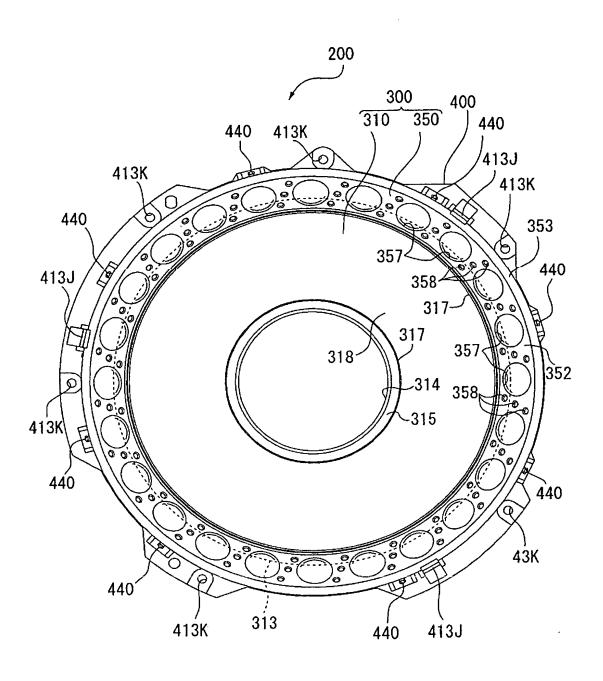
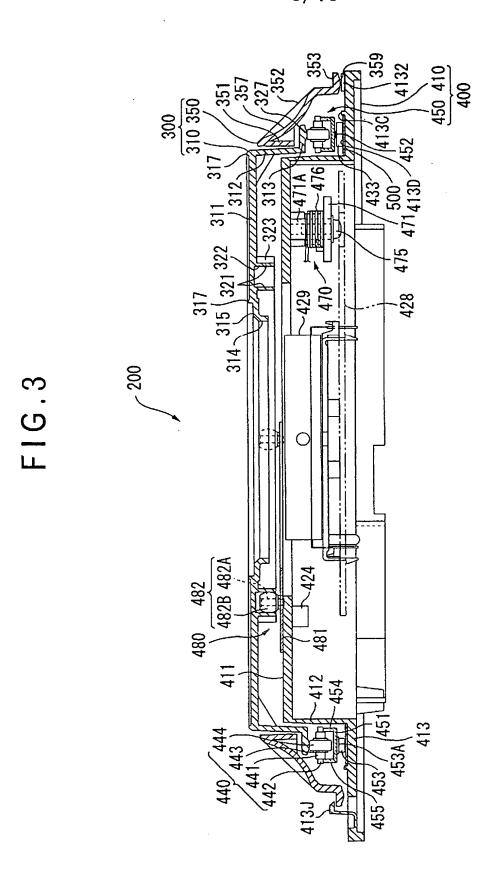
1/15



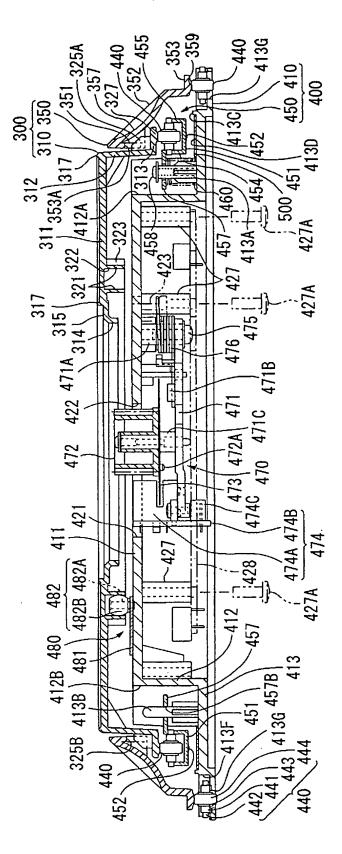
2/15 FIG.2



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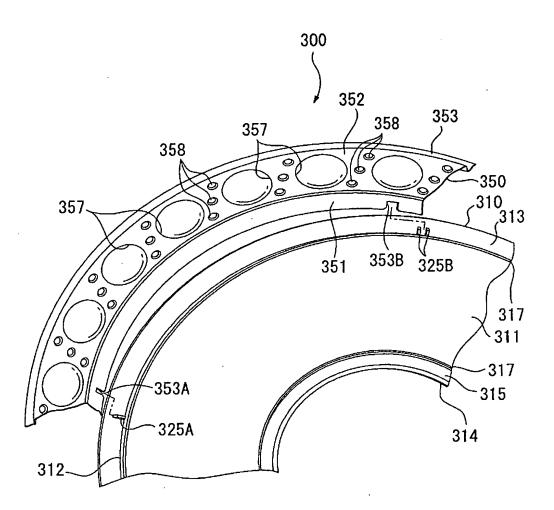
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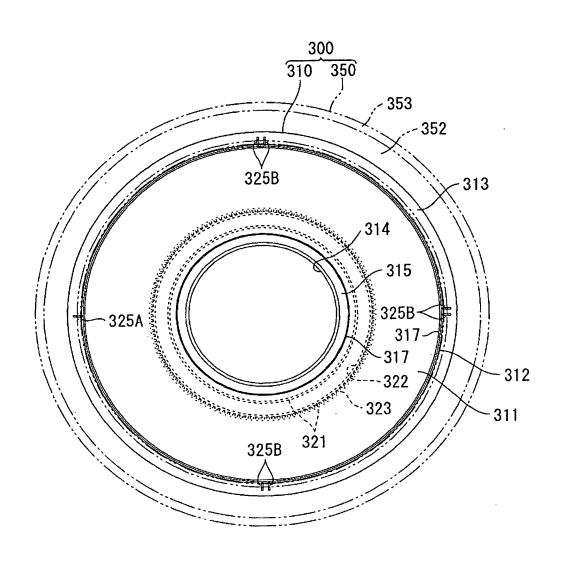
PROCESSING APPARATUS AND ... By: Yoshinori KATAOKA et al. Docket No.: 031047

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5/15 **FIG**.5

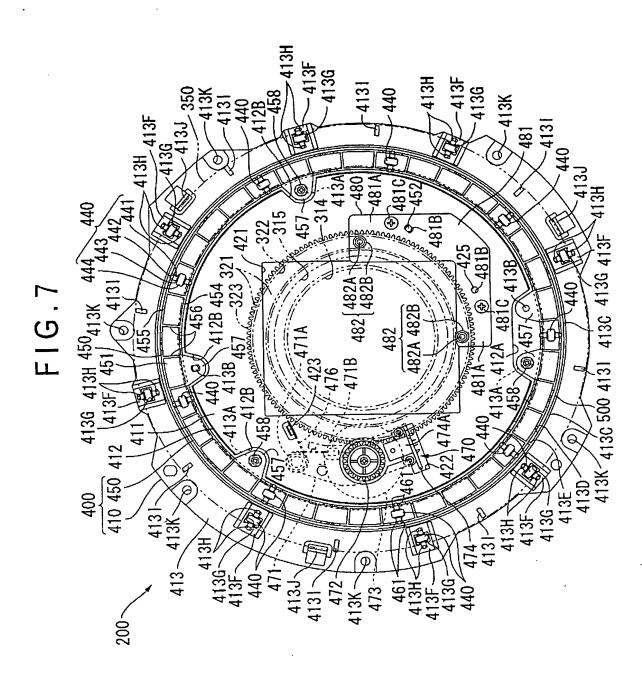


6/15 FIG.6



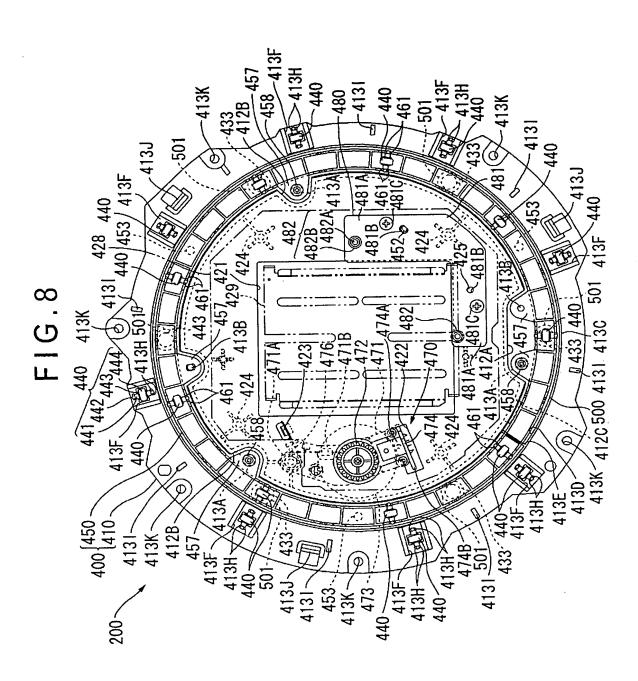
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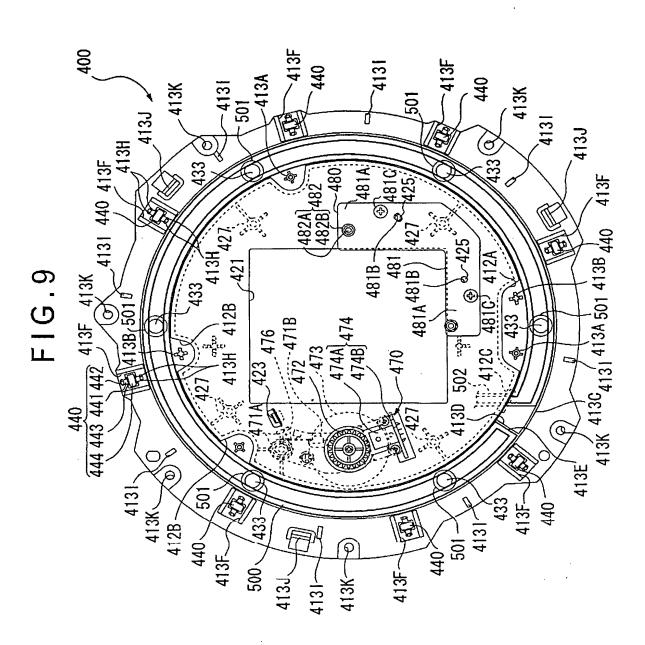
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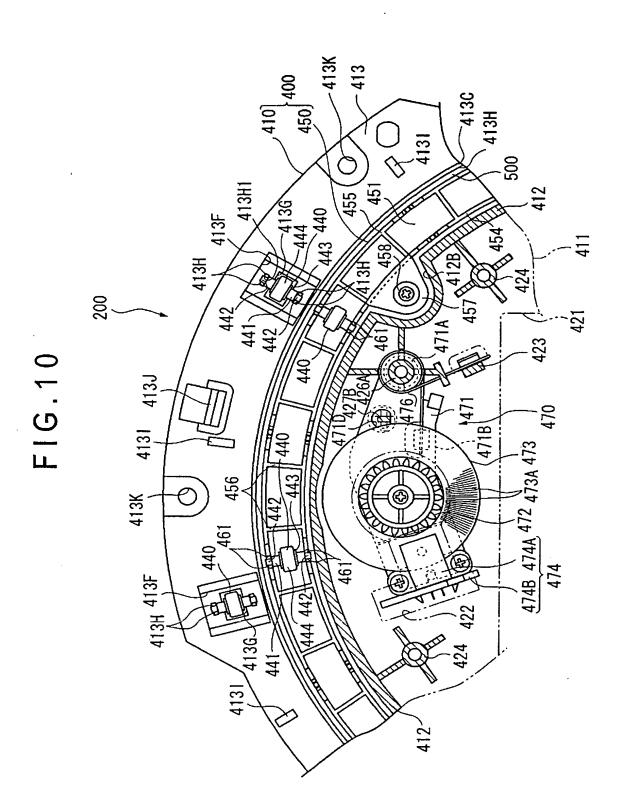
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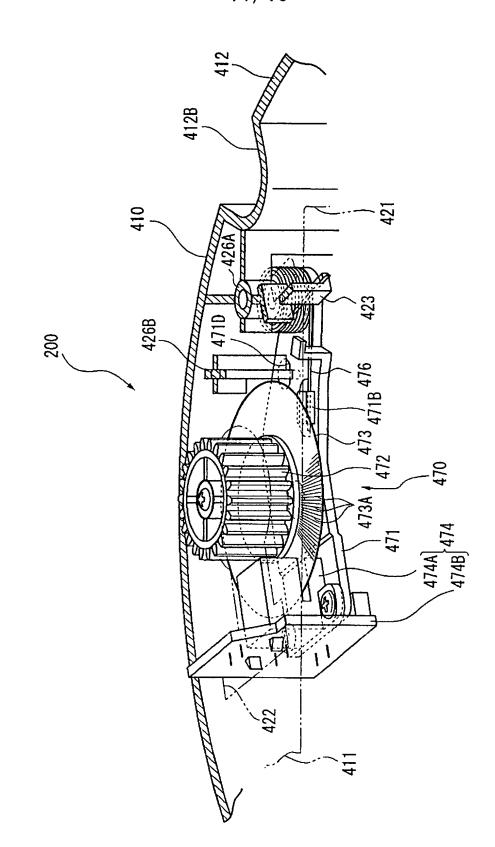
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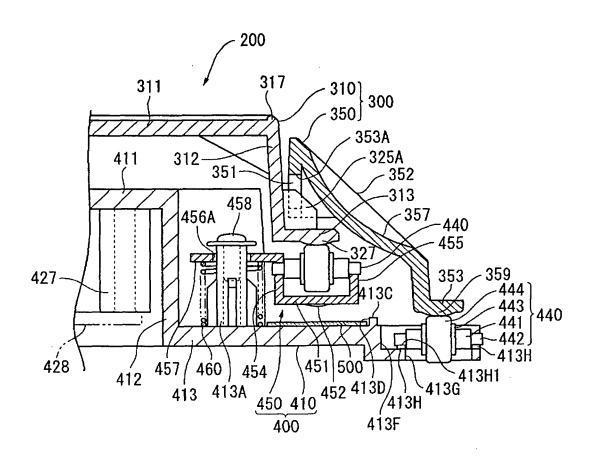
11/15



F1G.11

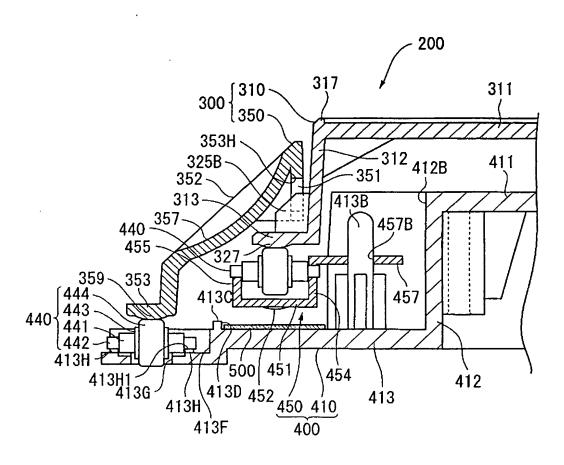
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12/15 FIG.12

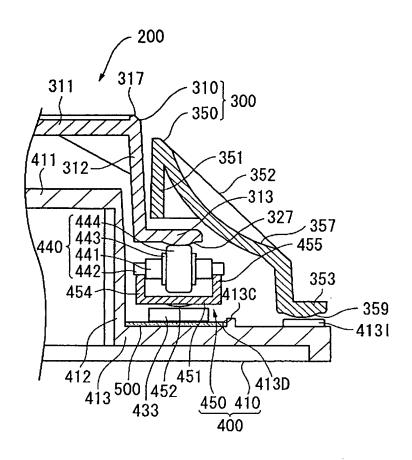


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13/15 FIG.13



14/15 FIG.14



15/15 FIG.15

